

Programme Regulations 2009/10

Degree of Master of Science in Microelectronics

Code: 5060

Notes:

- (i) *These programme regulations should be read in conjunction with the University's Masters Progress Regulations and Examination Conventions.*
- (ii) *A core module is a module which a student must pass, and in which a fail mark may neither be carried nor compensated; such modules are designated by the board of studies as essential for progression to a further stage of the programme or for study in a further module.*
- (iii) *A compulsory module is a module which a student must take.*

1. Programme structure

- (a) The programme is available for study in full-time mode only
- (b) The period of study for full-time mode shall be 1 year starting in September.
- (c) The programme comprises modules to a credit value of 180.
- (d) All candidates shall take the following compulsory modules:

<i>Code</i>	<i>Descriptive title</i>	<i>Total Credits</i>	<i>Credits Sem 1</i>	<i>Credits Sem 2</i>	<i>Credits Sem 3</i>	<i>Level</i>
EEE8004	System-on-Chip Test Strategies	15	15			7
EEE8018	Semiconductor Electronic Devices	15	15			7
EEE8019	Semiconductor Device Fabrication	15		15		7
EEE8020	Advanced VLSI Design	15		15		7
EEE8059	Coursework (Microelectronics)	10	10			7
EEE8096	Research Methods	20	10	10		7
EEE8097	Individual Project	60			60	7

- (e) All candidates shall take further optional modules to a value of 30 credits from the following:

<i>Code</i>	<i>Descriptive title</i>	<i>Total Credits</i>	<i>Credits Sem 1</i>	<i>Credits Sem 2</i>	<i>Credits Sem 3</i>	<i>Level</i>
EEE8001	Signal Processing and Estimation	15	15			7
EEE8002	Mobile and Cellular Communications	15		15		7
EEE8011	Optoelectronics	15		15		7
EEE8012	Power Electronics	15	15			7

2. Assessment methods

Details of the assessment pattern for each module are explained in the module outline.