

Programme Regulations 2009/10

Degree of Bachelor of Engineering with Honours in Electronics with Business Management

UCAS Code: H6NF

Notes

- (i) *These programme regulations should be read in conjunction with the University's Undergraduate Progress Regulations and Examination Conventions.*
- (ii) *All optional modules are offered subject to the constraints of the timetable and to any restrictions on the number of students who may be taught on a particular module. Not all modules may be offered in all years and they are listed subject to availability.*
- (iii) *A compulsory module is a module which a student is required to study.*
- (iv) *A Core module is a module which a student must pass, and in which a fail mark may neither be carried nor compensated; such modules are designated by the board of studies as essential for progression to a further stage of the programme or for study in a further module.*

See also:

Stage 0 (Foundation Year) for all Degrees of Bachelor of Engineering with Honours and Master of Engineering with Honours

1. Stage 0

Candidates who do not meet the requirements for entry into Stage 1 may with approval of the Degree Programme Director commence this degree programme at Stage 0 and shall proceed under the regulations relating to Stage 0.

2. Stage 1

(a) All candidates shall take the following compulsory modules:

| Code | Descriptive title | Total Credits | Credits Sem 1 | Credits Sem 2 | Level | Type |
|---------|---|---------------|---------------|---------------|-------|------|
| BUS1001 | Introduction to Management and Organisation | 20 | 10 | 10 | 4 | |
| EEE1002 | Electronics I | 20 | 20 | | 4 | |
| EEE1003 | Circuit Theory | 20 | 20 | | 4 | |
| EEE1004 | Project | 20 | 10 | 10 | 4 | |
| EEE1005 | Signals and Communications I | 20 | | 20 | 4 | |
| ENG1001 | Engineering Mathematics I | 20 | 10 | 10 | 4 | |

3. Stage 2

(a) All candidates shall take the following compulsory modules:

| Code | Descriptive title | Total | Credits | Credits | Level | Type |
|------|-------------------|-------|---------|---------|-------|------|
|------|-------------------|-------|---------|---------|-------|------|

| | | <i>Credits</i> | <i>Sem 1</i> | <i>Sem 2</i> | | |
|---------|--|----------------|--------------|--------------|---|--|
| BUS2012 | Human Resource Management | 10 | 10 | | 6 | |
| BUS2013 | Human Resource Management in Practice | 10 | | 10 | 6 | |
| BUS2014 | Business Systems | 20 | 10 | 10 | 6 | |
| EEE2004 | Information Theory and Coding | 10 | | 10 | 5 | |
| EEE2006 | Electronics II | 20 | 20 | | 5 | |
| EEE2007 | Computer Systems and Microprocessors | 20 | | 20 | 5 | |
| EEE2008 | Project and Professional Issues | 20 | | 20 | 5 | |
| EEE2011 | Discrete Mathematics for Computing Science | 10 | 10 | | 5 | |

4. Stage 3

(a) All candidates shall take the following compulsory modules:

| Code | Descriptive title | Total Credits | Credits Sem 1 | Credits Sem 2 | Level | Type |
|---------|--|---------------|---------------|---------------|-------|------|
| BUS3021 | International Human Resource Management | 10 | | 10 | 6 | |
| BUS3022 | Strategic Human Resource Management | 10 | 10 | | 6 | |
| BUS3030 | Intro to Operations Management | 10 | 10 | | 6 | |
| BUS3031 | Management Practice Business Game (semester 2 version) | 10 | | 10 | 6 | |

(b) All candidates shall take **one** of the following optional modules:

| <i>Code</i> | <i>Descriptive title</i> | <i>Total Credits</i> | <i>Credits Sem 1</i> | <i>Credits Sem 2</i> | <i>Level</i> | <i>Type</i> |
|-------------|--|----------------------|----------------------|----------------------|--------------|-------------|
| EEE3095 | Individual Project and Dissertation (BEng) | 40 | 20 | 20 | 6 | |
| EEE3096 | Individual Project and Dissertation (BEng) | 40 | 10 | 30 | 6 | |
| EEE3097 | Individual Project and Dissertation (BEng) | 40 | 30 | 10 | 6 | |

(c) Candidates shall select optional modules with a total value of 40 credits, from the following list:

| <i>Code</i> | <i>Descriptive title</i> | <i>Total Credits</i> | <i>Credits Sem 1</i> | <i>Credits Sem 2</i> | <i>Level</i> | <i>Type</i> |
|-------------|------------------------------------|----------------------|----------------------|----------------------|--------------|-------------|
| EEE3001 | Design of Linear Control Systems | 10 | 10 | | 6 | |
| EEE3007 | Design and Test of Digital Systems | 10 | 10 | | 6 | |
| EEE3008 | Industrial Automation and Robotics | 10 | 10 | | 6 | |
| EEE3009 | Real Time and Embedded Systems | 10 | 10 | | 6 | |
| EEE3012 | Integrated Circuit Design | 10 | | 10 | 6 | |

| | | | | | | |
|---------|-------------------------------------|----|--|----|---|--|
| EEE3013 | Image Processing and Machine Vision | 10 | | 10 | 6 | |
| EEE3015 | Telecommunication Networks | 10 | | 10 | 6 | |
| EEE3016 | Optoelectronics | 10 | | 10 | 6 | |

With the permission of the Degree Programme Director, other module combinations may be selected.

5. Assessment methods

Details of the assessment pattern in each module are explained in the module outline.

6. Degree classification

Candidates will be assessed for the degree classification on the basis of all the modules taken at Stages 2 and 3 with the weightings of the stages being 1:3 for Stage 2 and Stage 3 respectively.